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DATE: 50420250

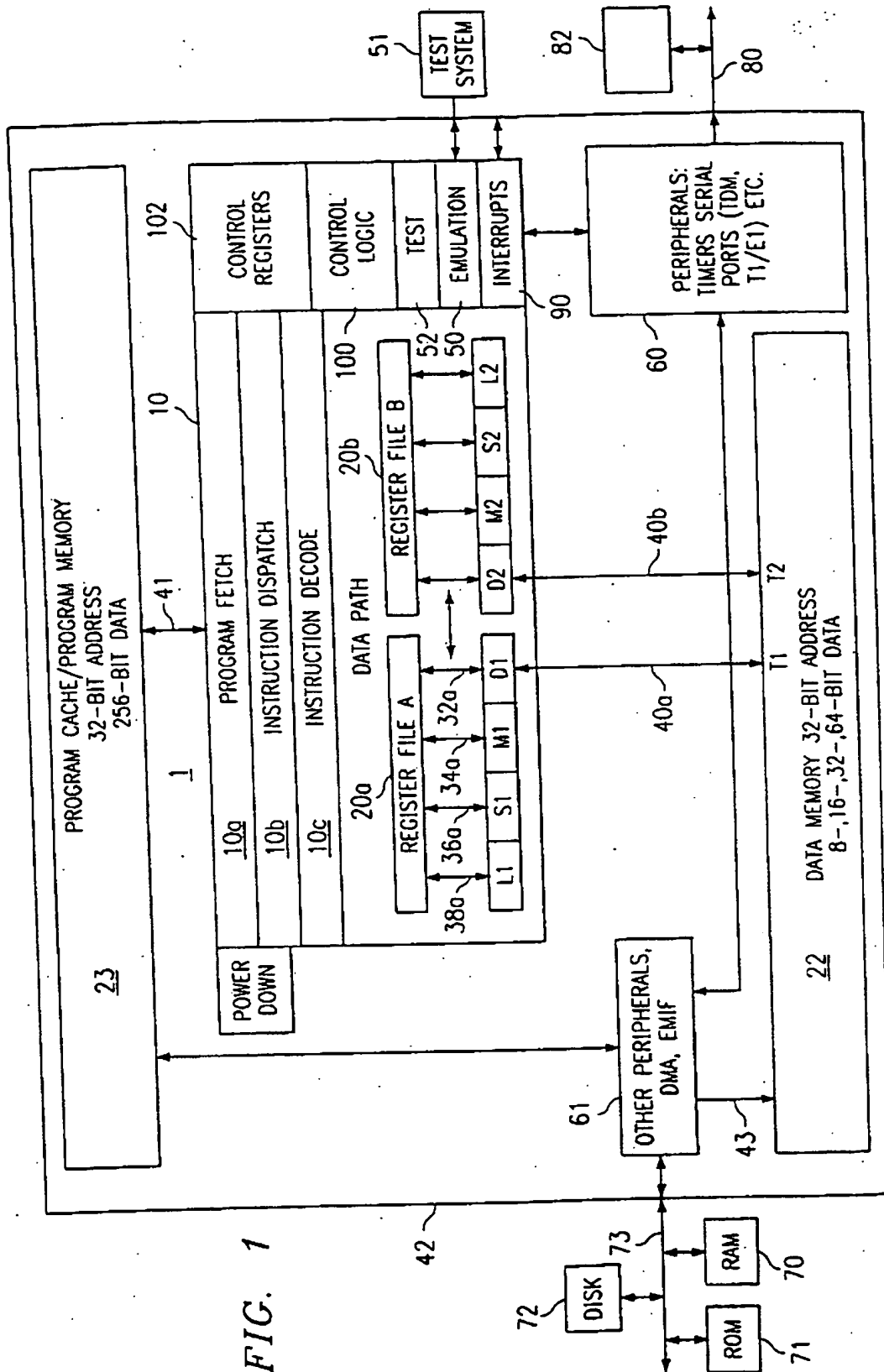
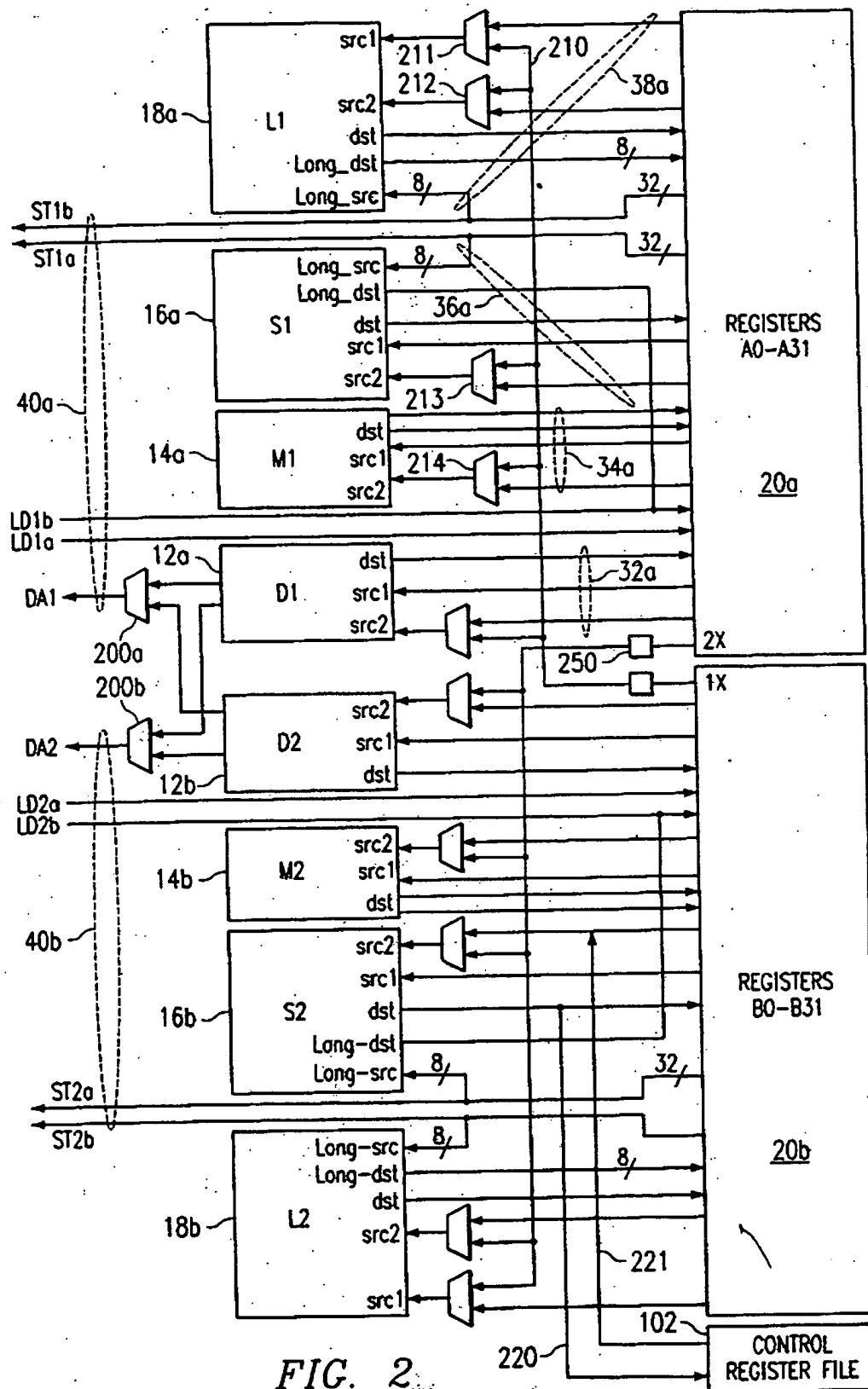
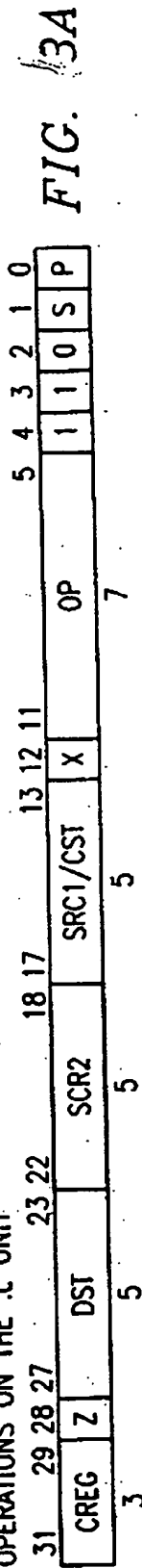


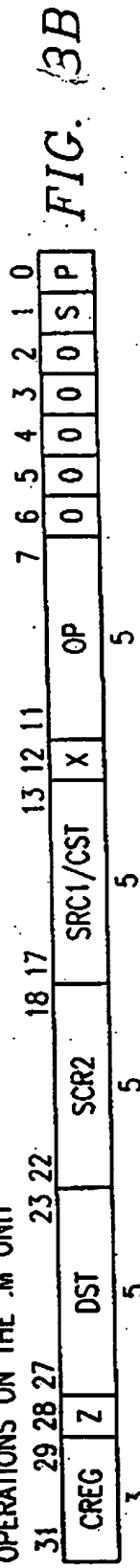
FIG. 2.



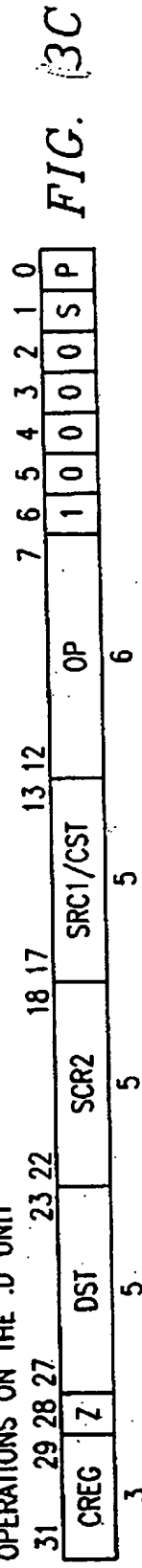
OPERATIONS ON THE .L UNIT



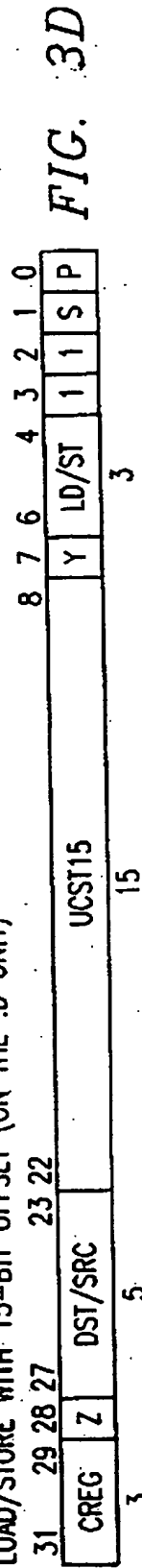
OPERATIONS ON THE .M UNIT



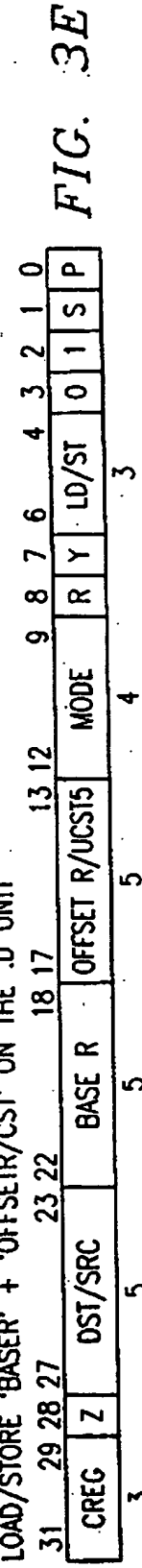
OPERATIONS ON THE .D UNIT



LOAD/STORE WITH 15-BIT OFFSET (ON THE .D UNIT)



LOAD/STORE 'BASER' + 'OFFSETR/CST' ON THE .D UNIT



OPERATIONS ON THE .S UNIT

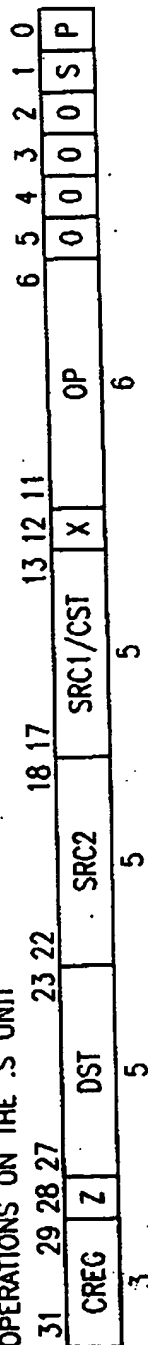


FIG. 3F

ADDK ON THE .S UNIT

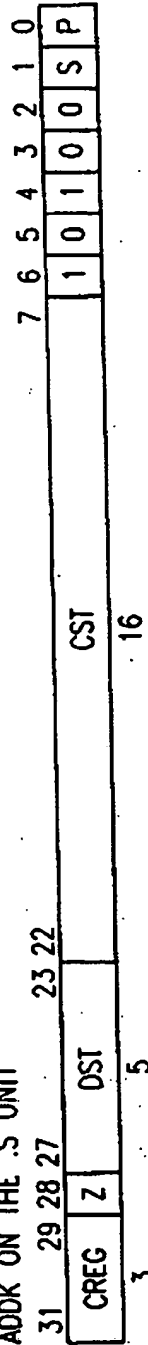


FIG. 3G

BITFIELD OPERATIONS (IMMEDIATE FORMS) ON THE .S UNIT

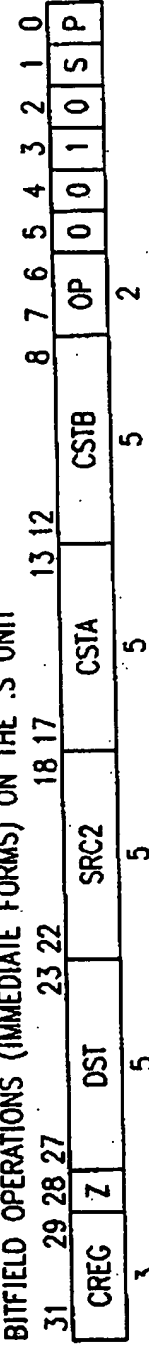


FIG. 3H

MVK AND MVKH ON THE .S UNIT

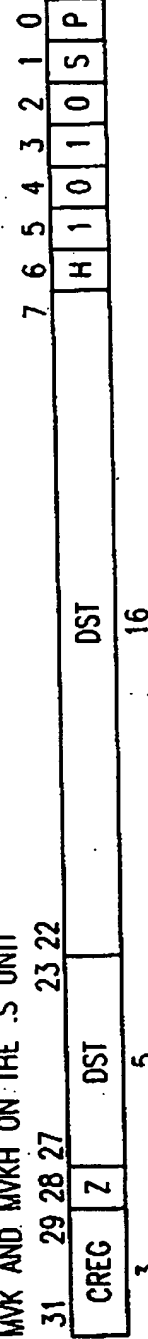


FIG. 3I

BCOND DISP ON THE .S UNIT

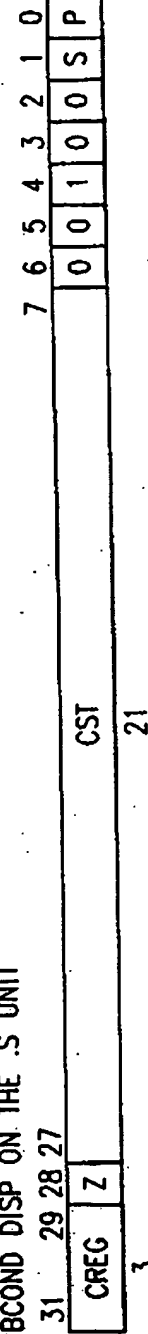


FIG. 3J

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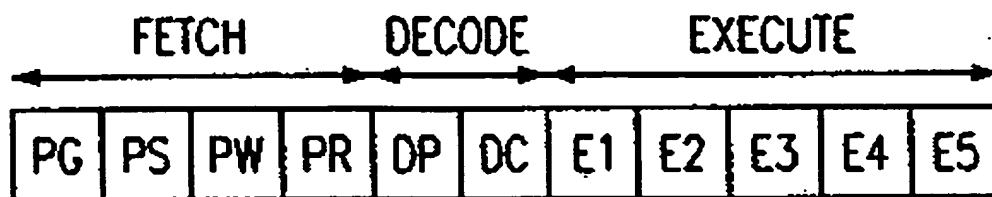


FIG. 4

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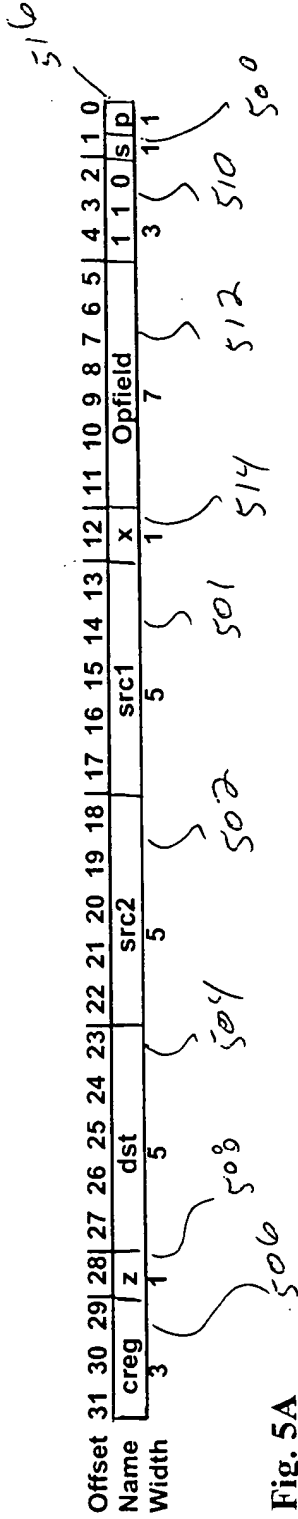


Fig. 5A

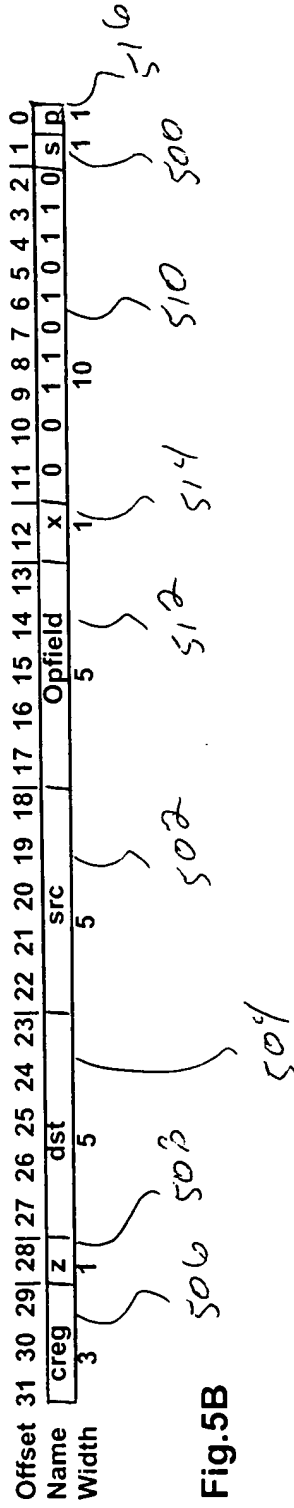


Fig. 5B

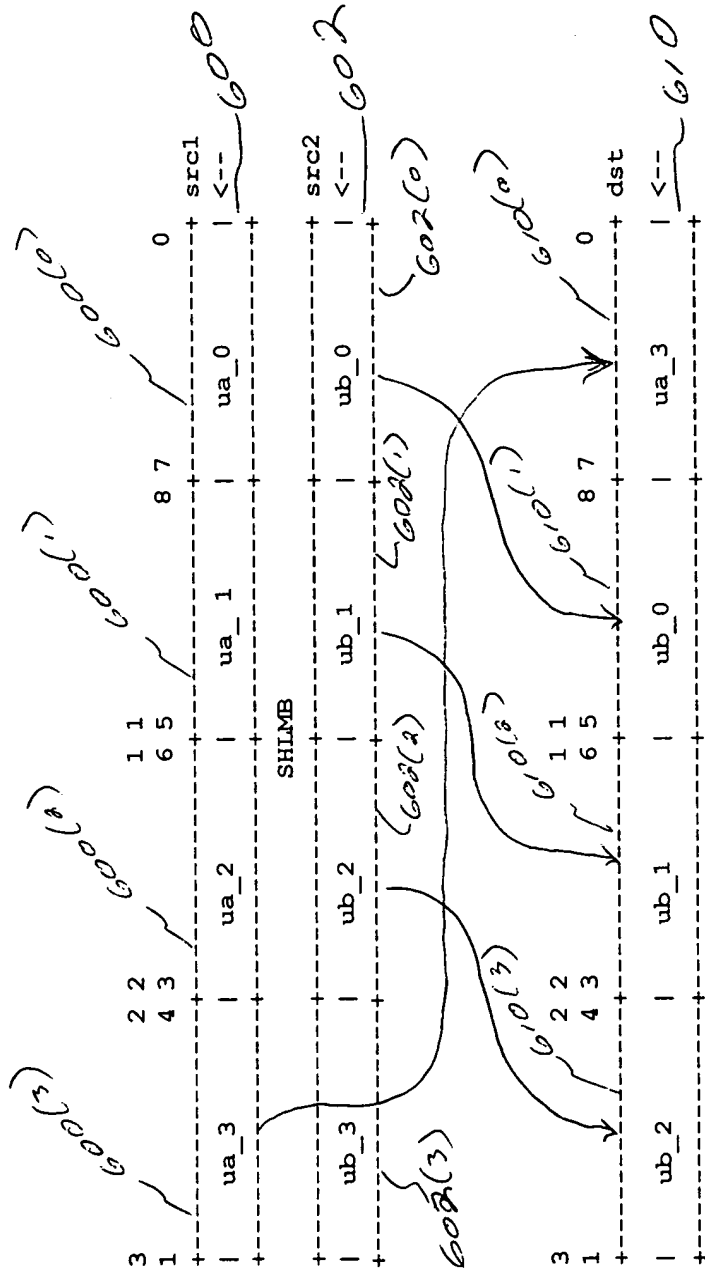


Fig. 6A

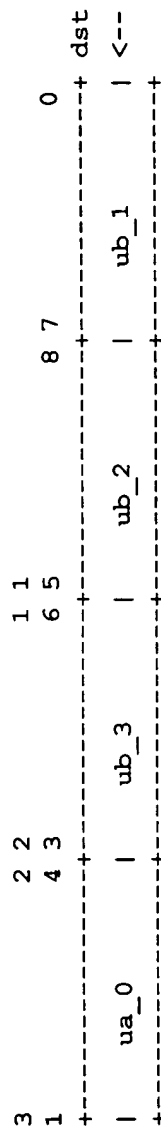


Fig. 6B

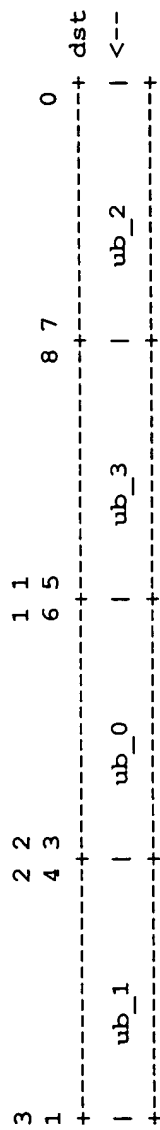


Fig. 6C

3	2	2	1	1	0	dst
1	4	3	6	5		

	ub_2		ub_3		ub_0	
					ub_1	
						<--

Fig. 6D

3	2	2	1	1	0	dst
1	4	3	6	5		

	ua_1		ua_0		ub_1	
					ub_0	
						<--

Fig. 6E

3	2	2	1	1	0	dst
1	4	3	6	5		

	ua_3		ua_2		ub_3	
					ub_2	
						<--

Fig. 6F

3	2	2	1	1	0	dst
1	4	3	6	5		

	ua_3		ua_1		ub_3	
					ub_1	
						<--

Fig. 6G

3	2	2	1	1	0	dst
1	4	3	6	5		

	ua_3		ua_2		ub_1	
					ub_0	
						<--

Fig. 6H

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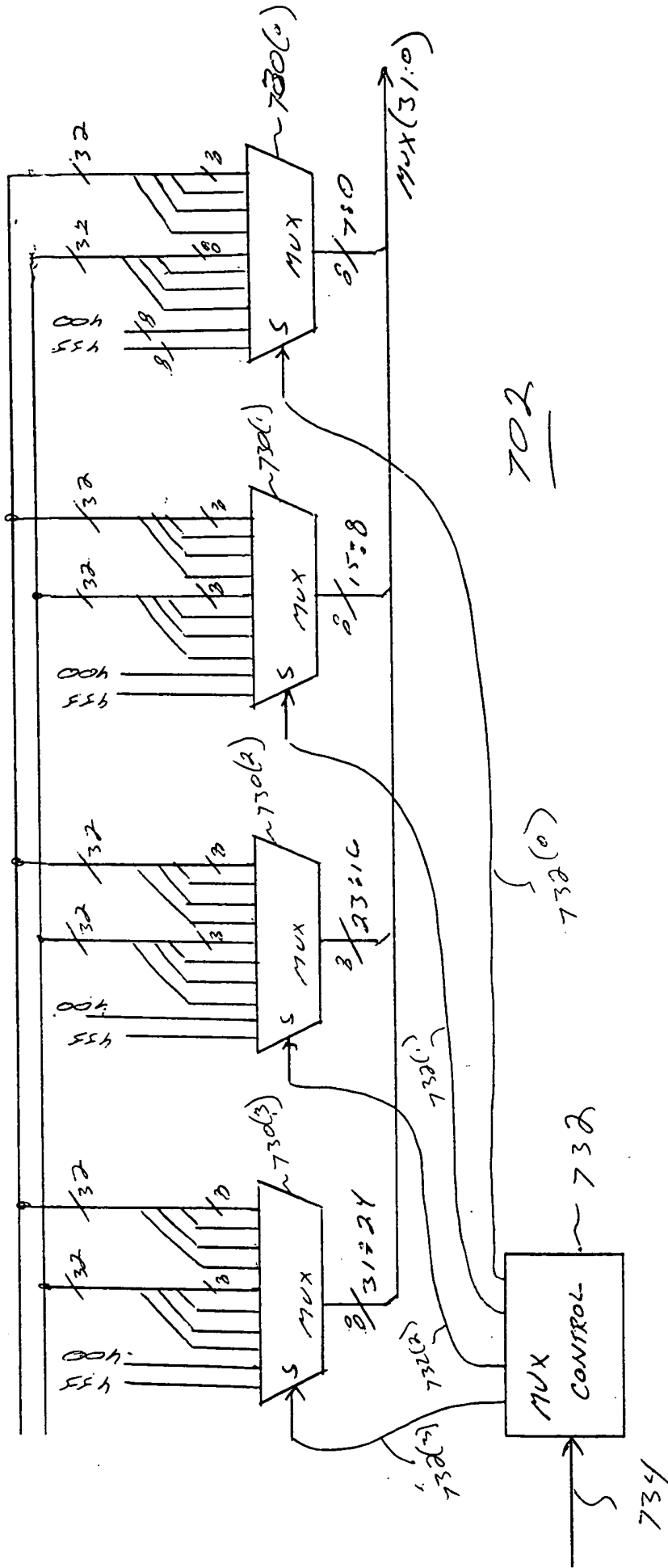


Fig 7B

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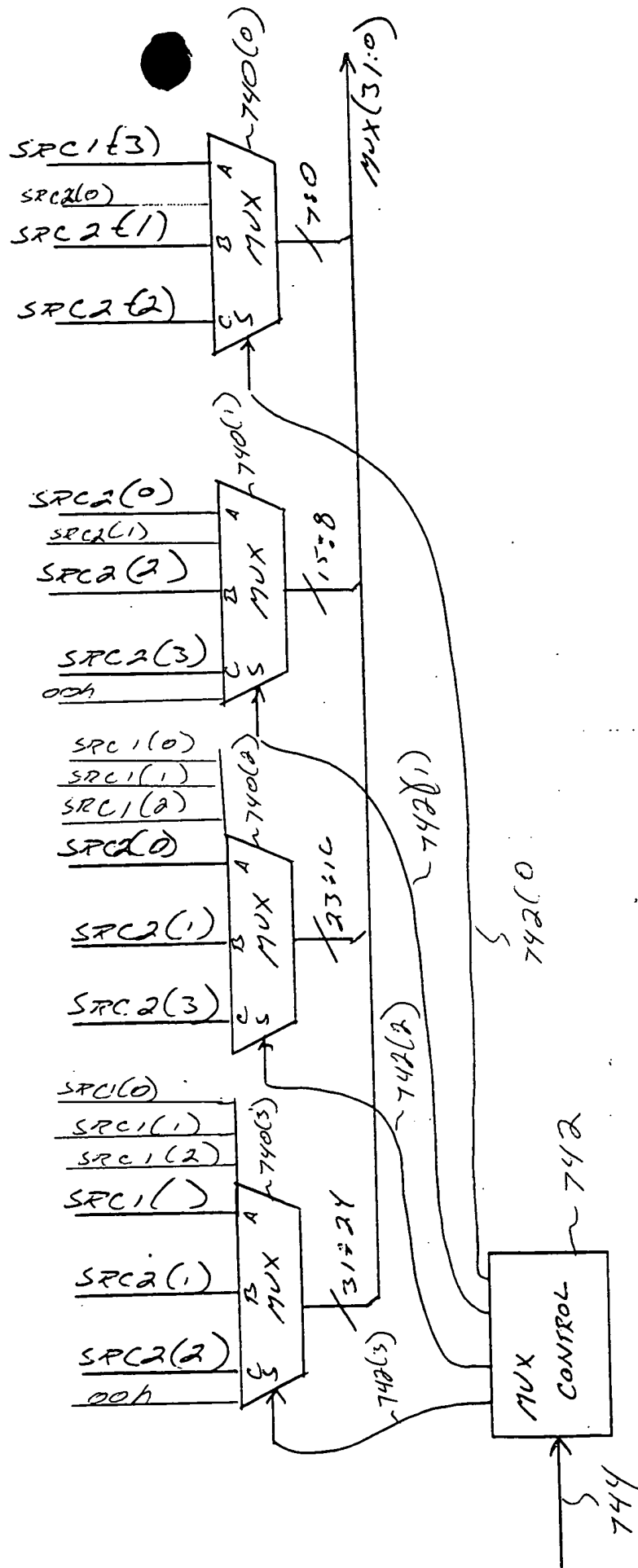


Fig. 7C

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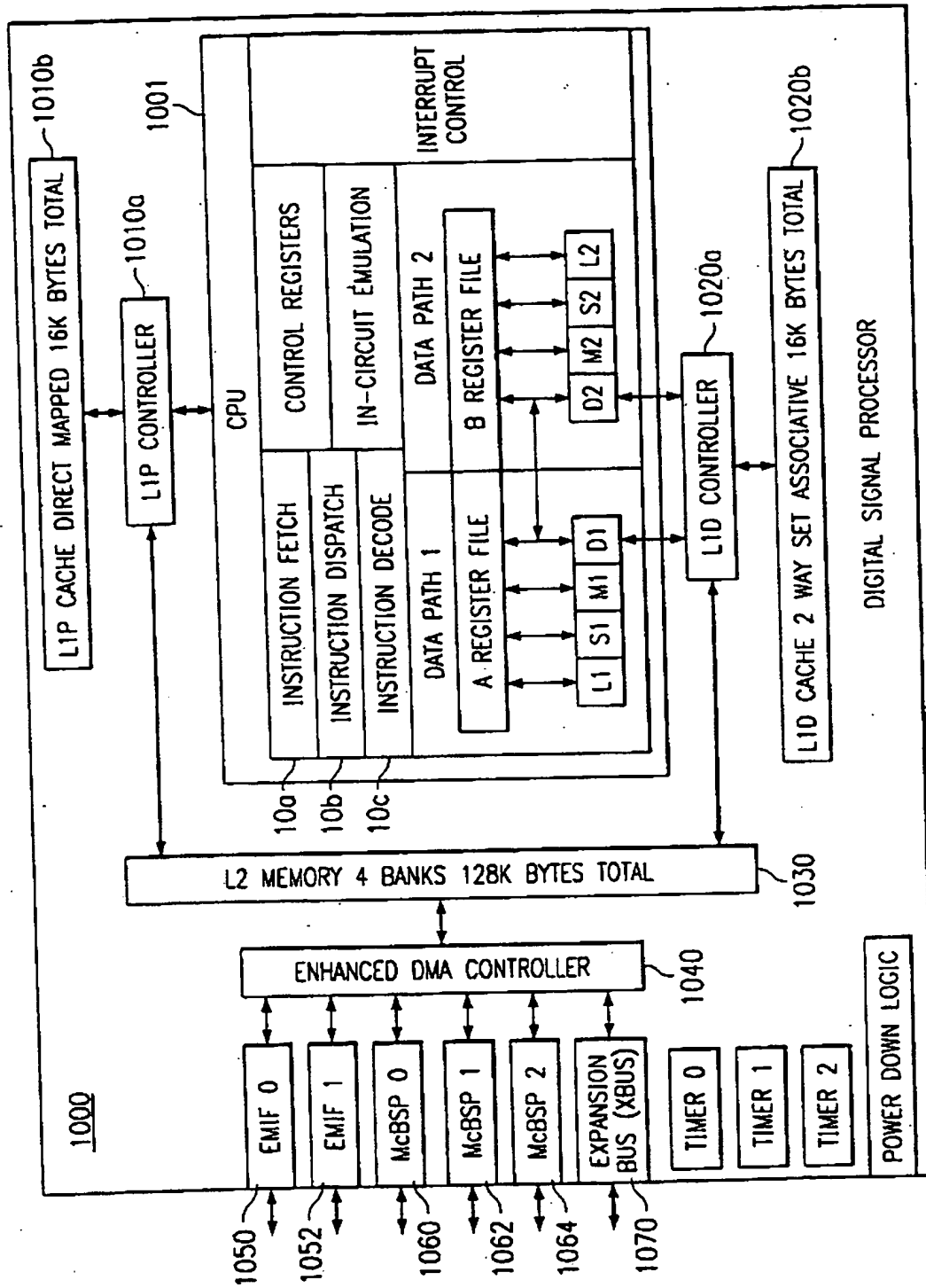


FIG. 3

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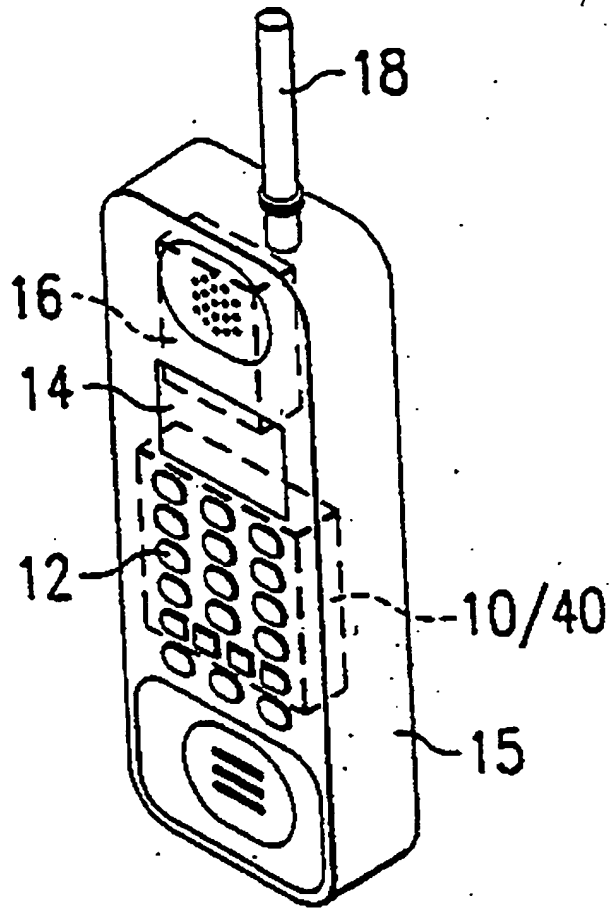


Fig 9

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